

1/16

FIG. 1

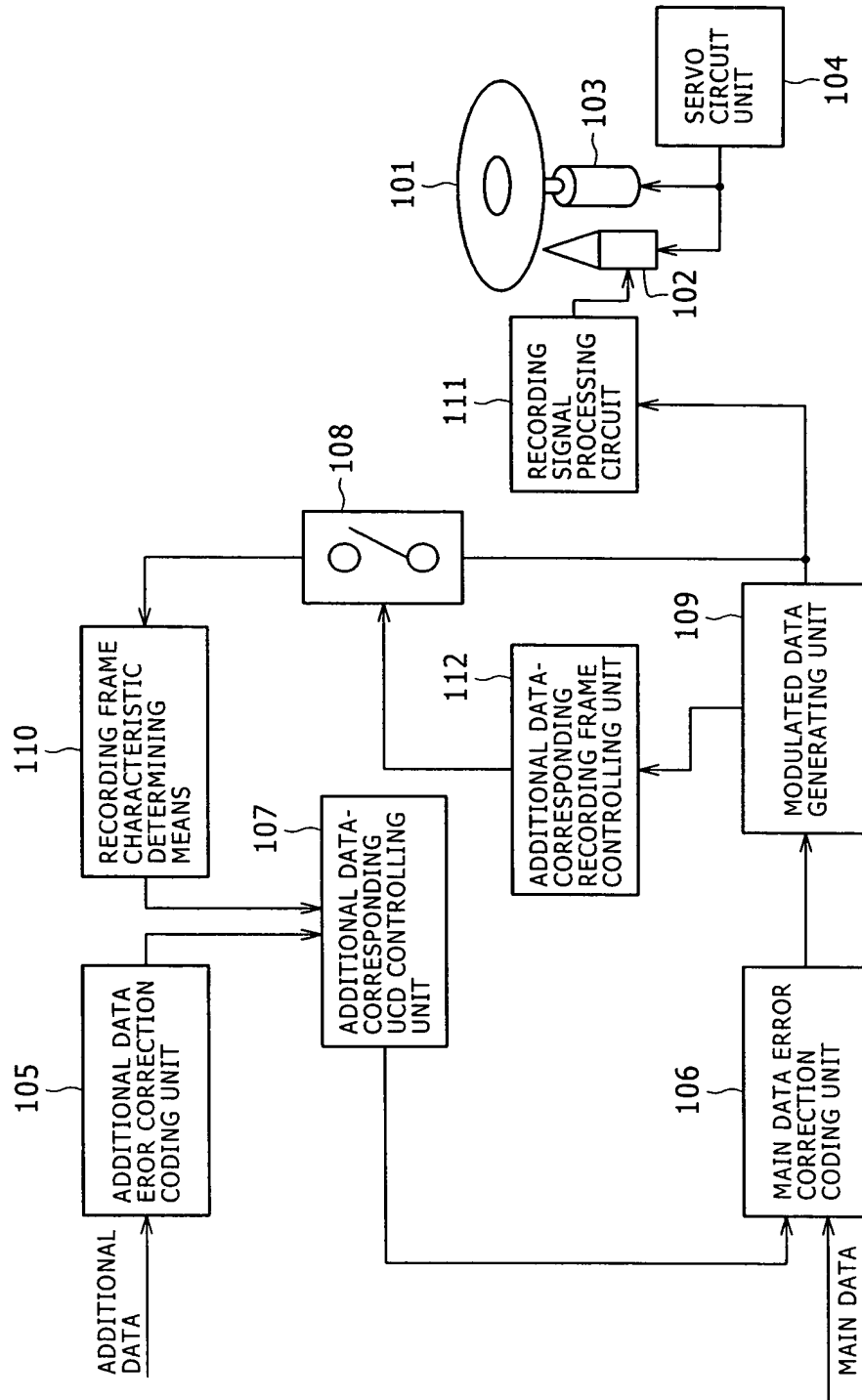


FIG. 2A

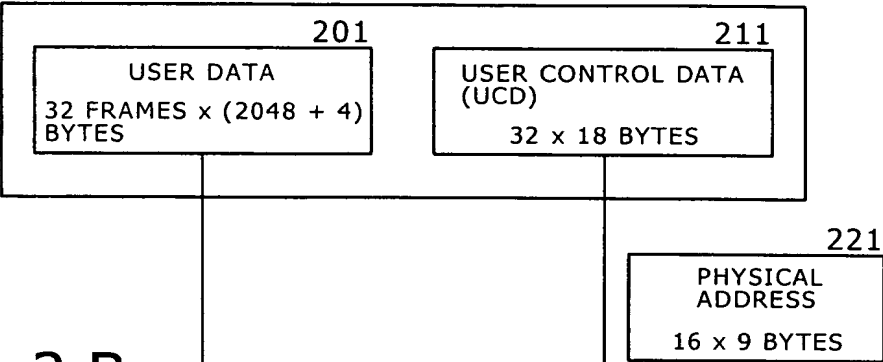


FIG. 2B

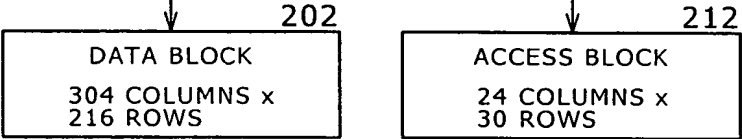


FIG. 2C

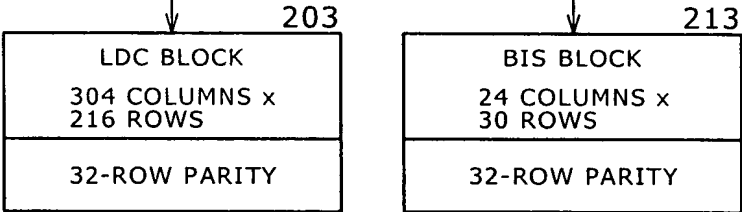


FIG. 2D

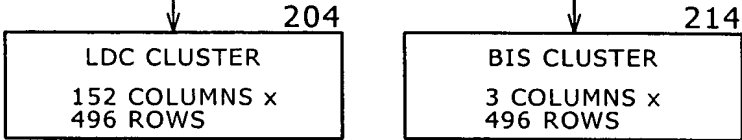


FIG. 3 D

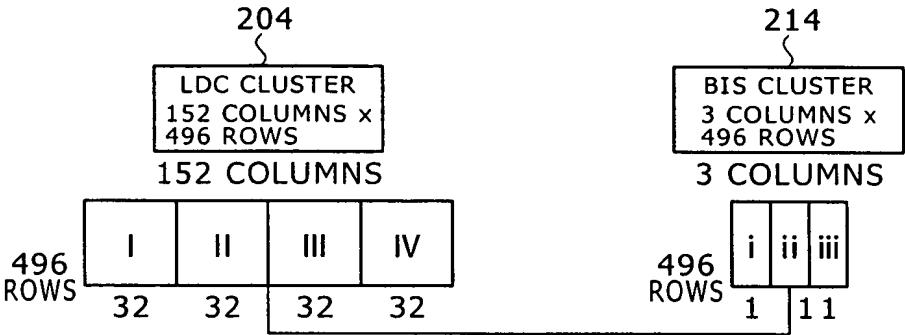


FIG. 3 E

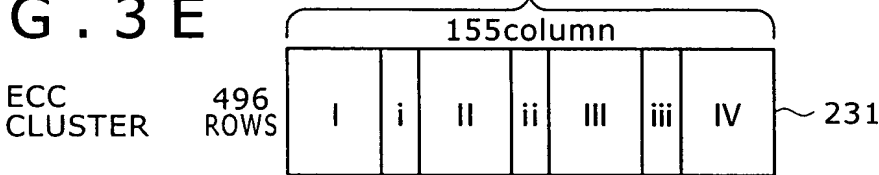


FIG. 3 F

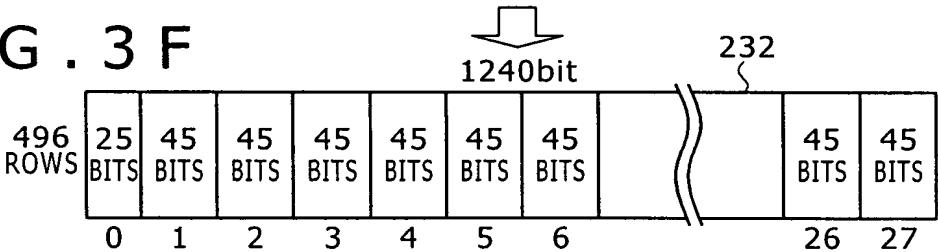


FIG. 3 G

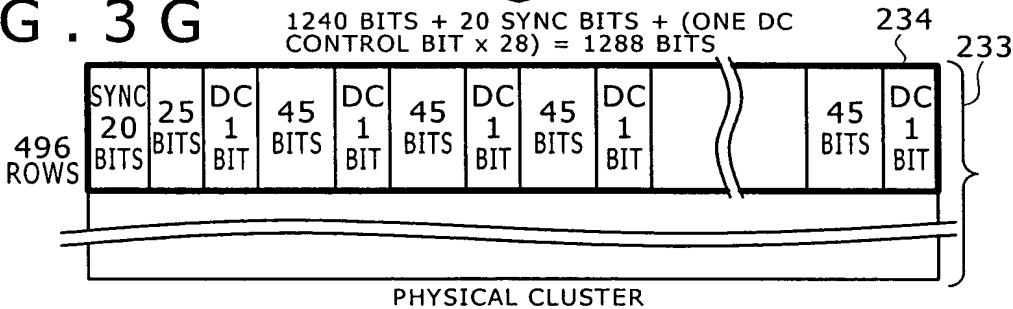
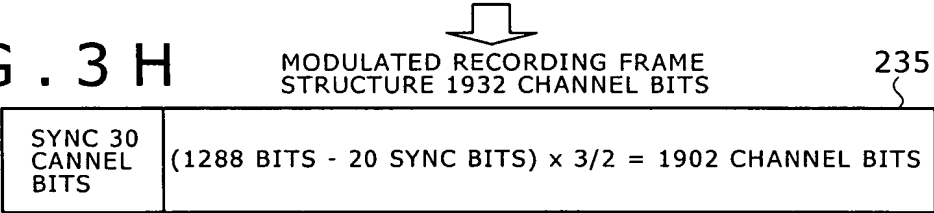


FIG. 3 H



4/16

FIG. 4

INPUT DATA BITS	MODULATED DATA BITS
00000000	010100100100
00001000	000100100100
0000000	010100000
000001	010100100
000010	000100000
000011	000100100
0001	000100
0010	010000
0011	010100
01	010
10	001
11	000 PRECEDING MODULATED BITS = XX1 101 PRECEDING MODULATED BITS = XX0

FIG. 5

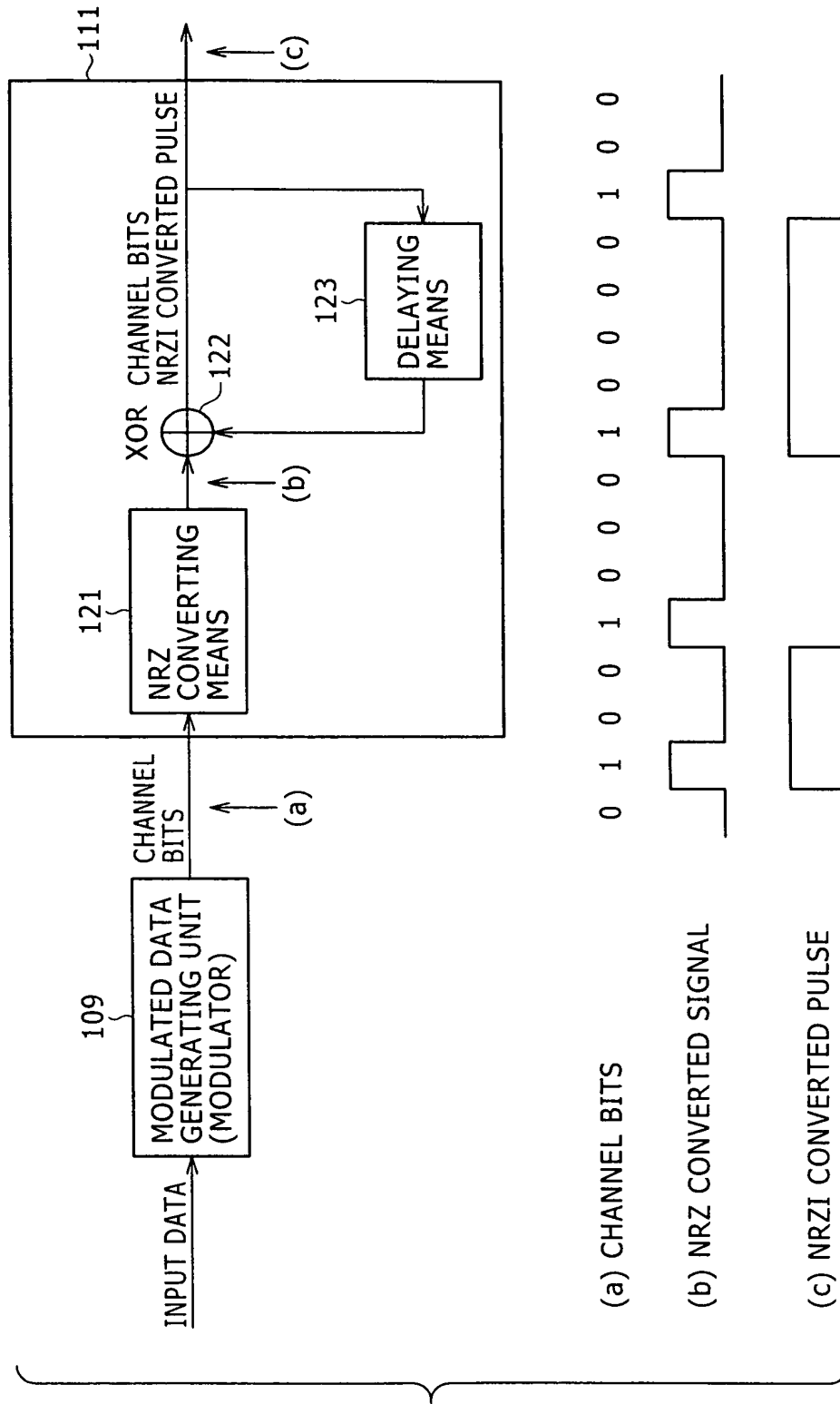
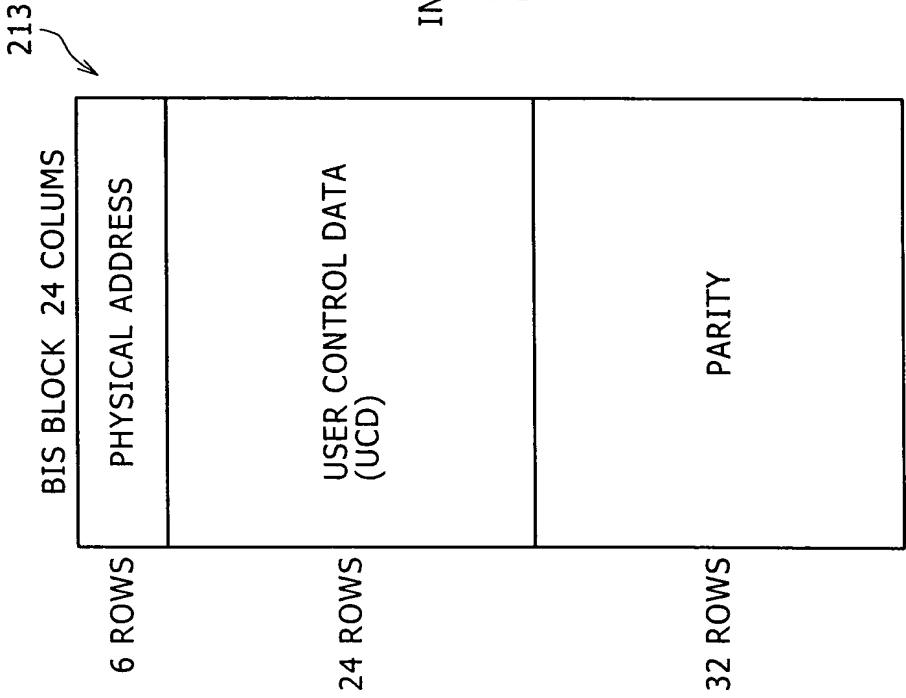


FIG. 6A



INTERLEAVE

FIG. 6B

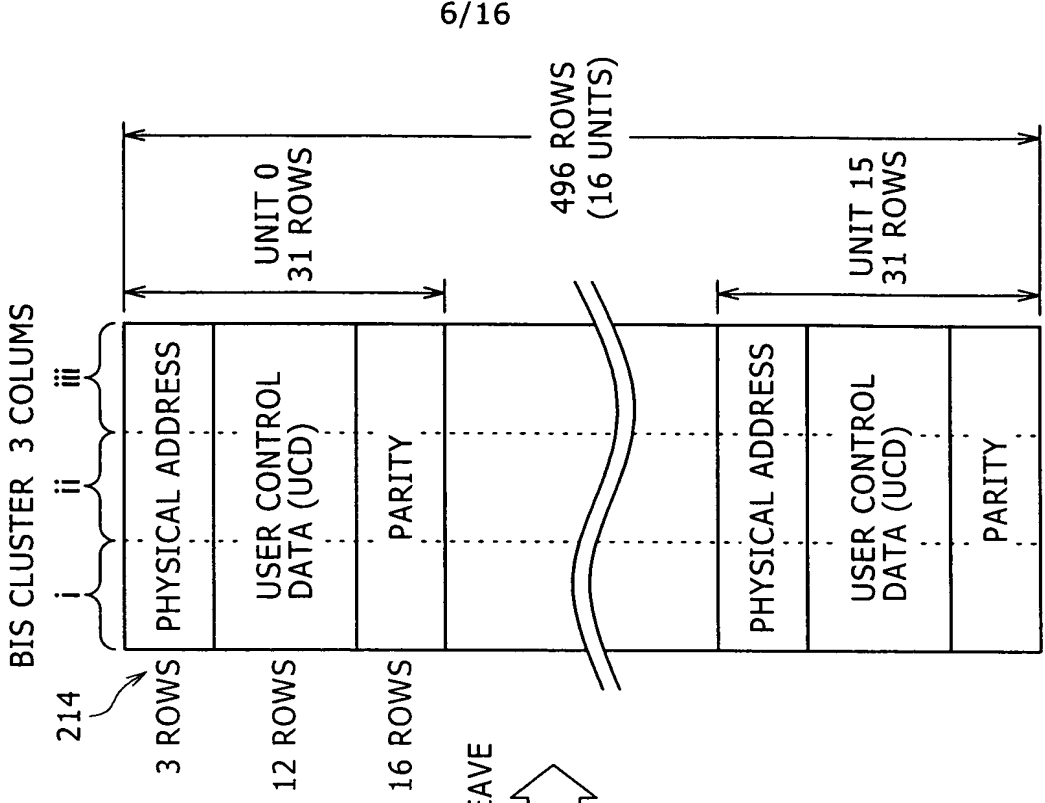
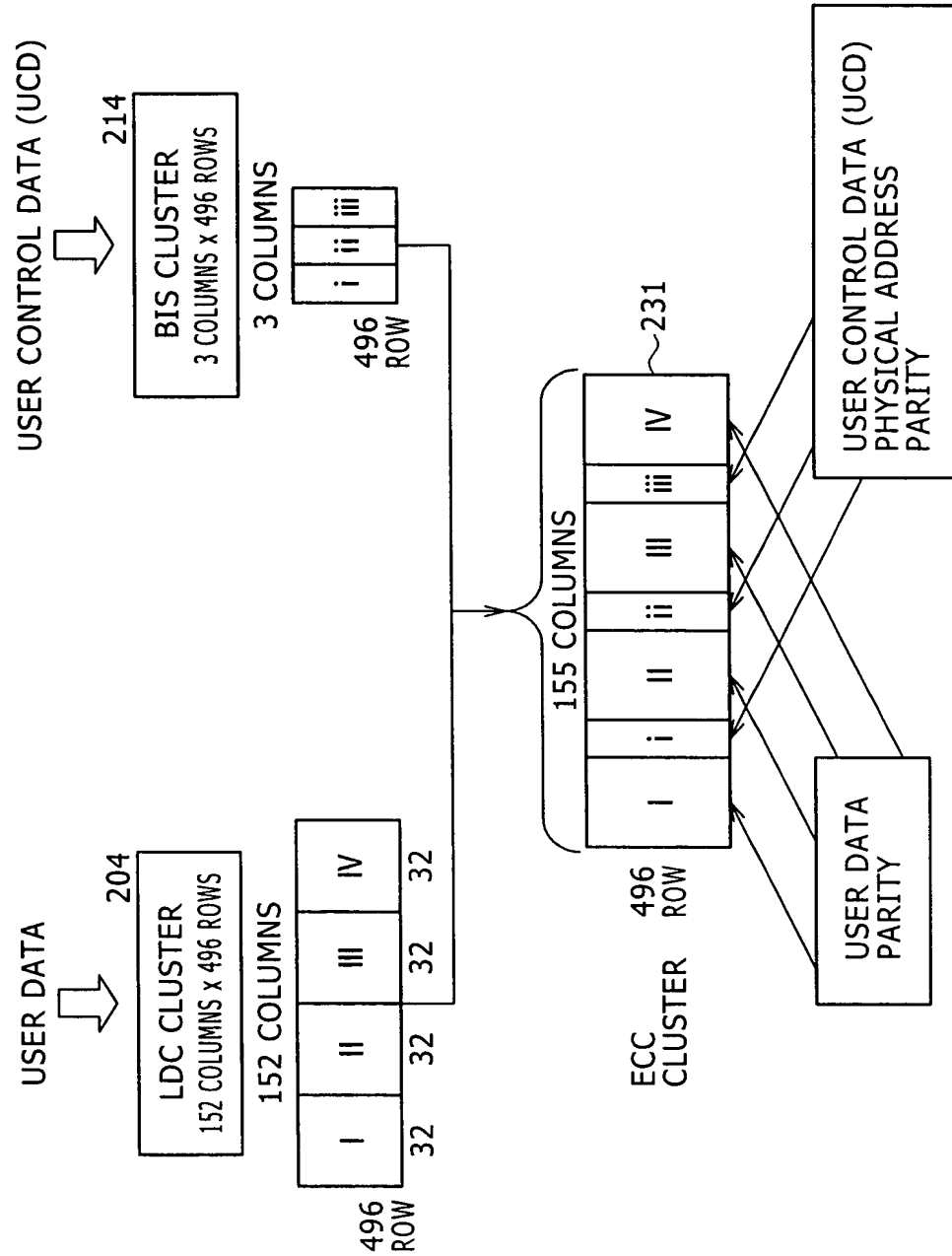


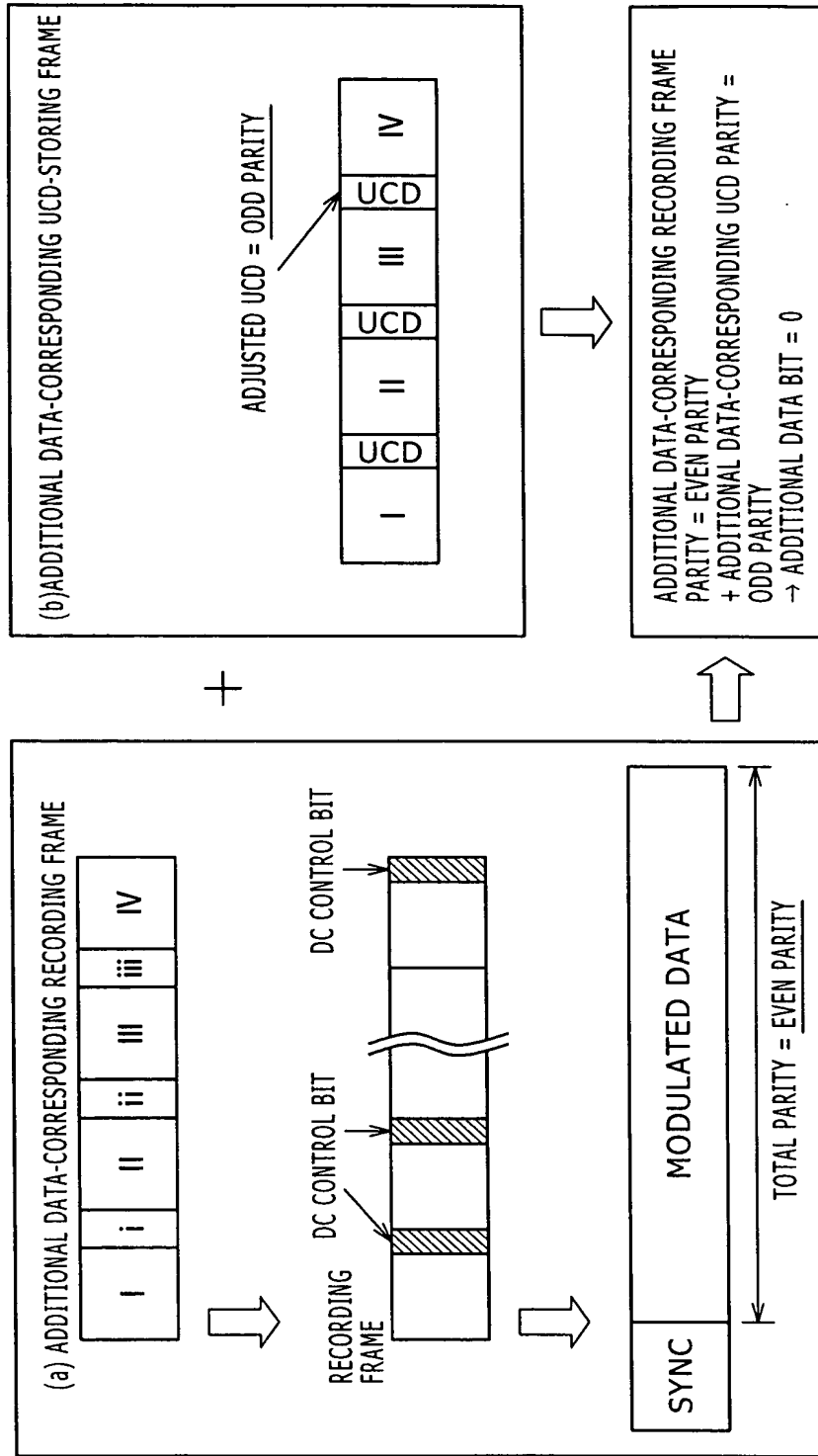
FIG. 7



9/16

FIG. 9

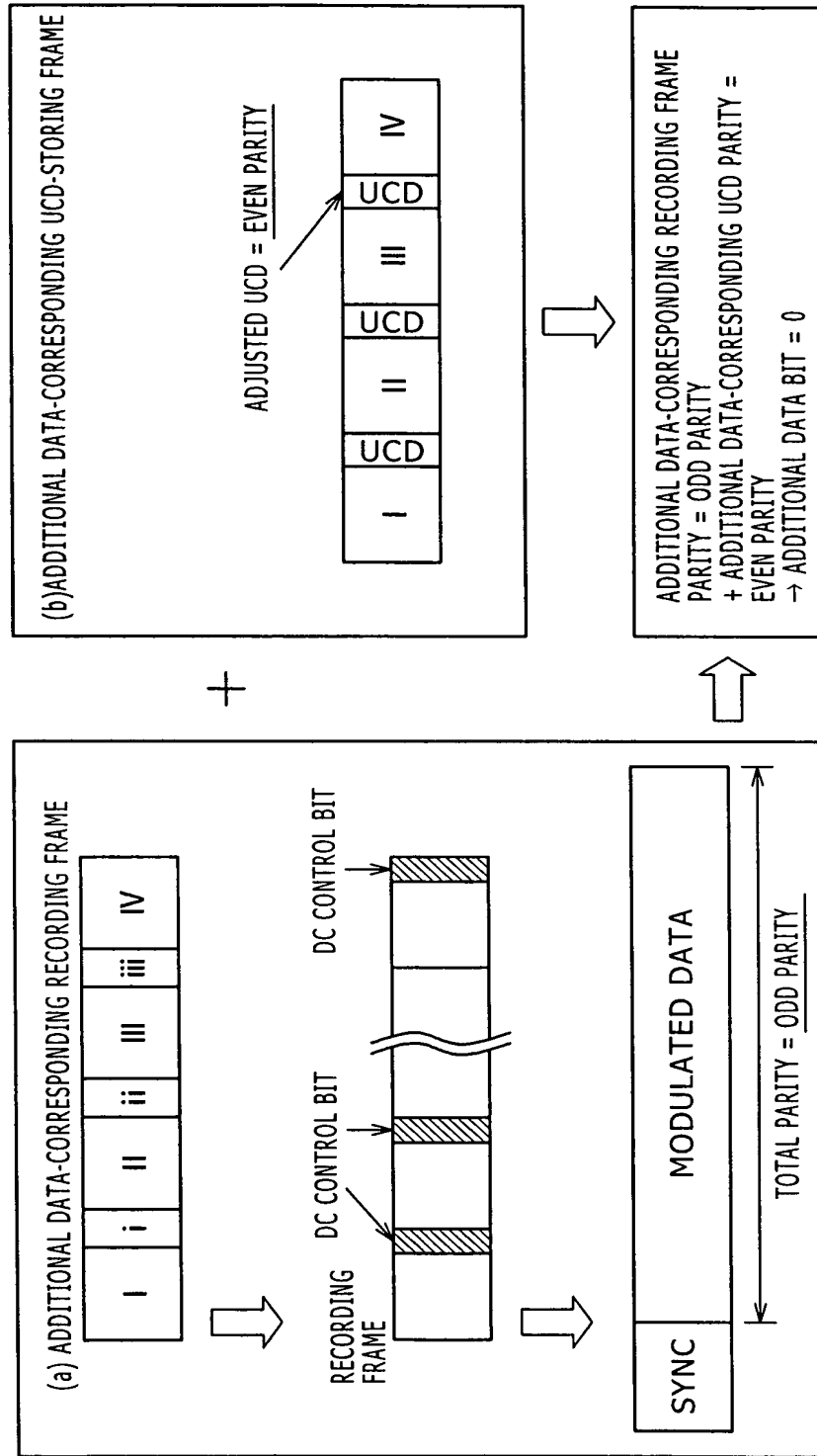
(1) ADDITIONAL DATA BIT = 0,
 ADDITIONAL DATA-CORRESPONDING RECORDING FRAME PARITY = EVEN



10/16

FIG. 10

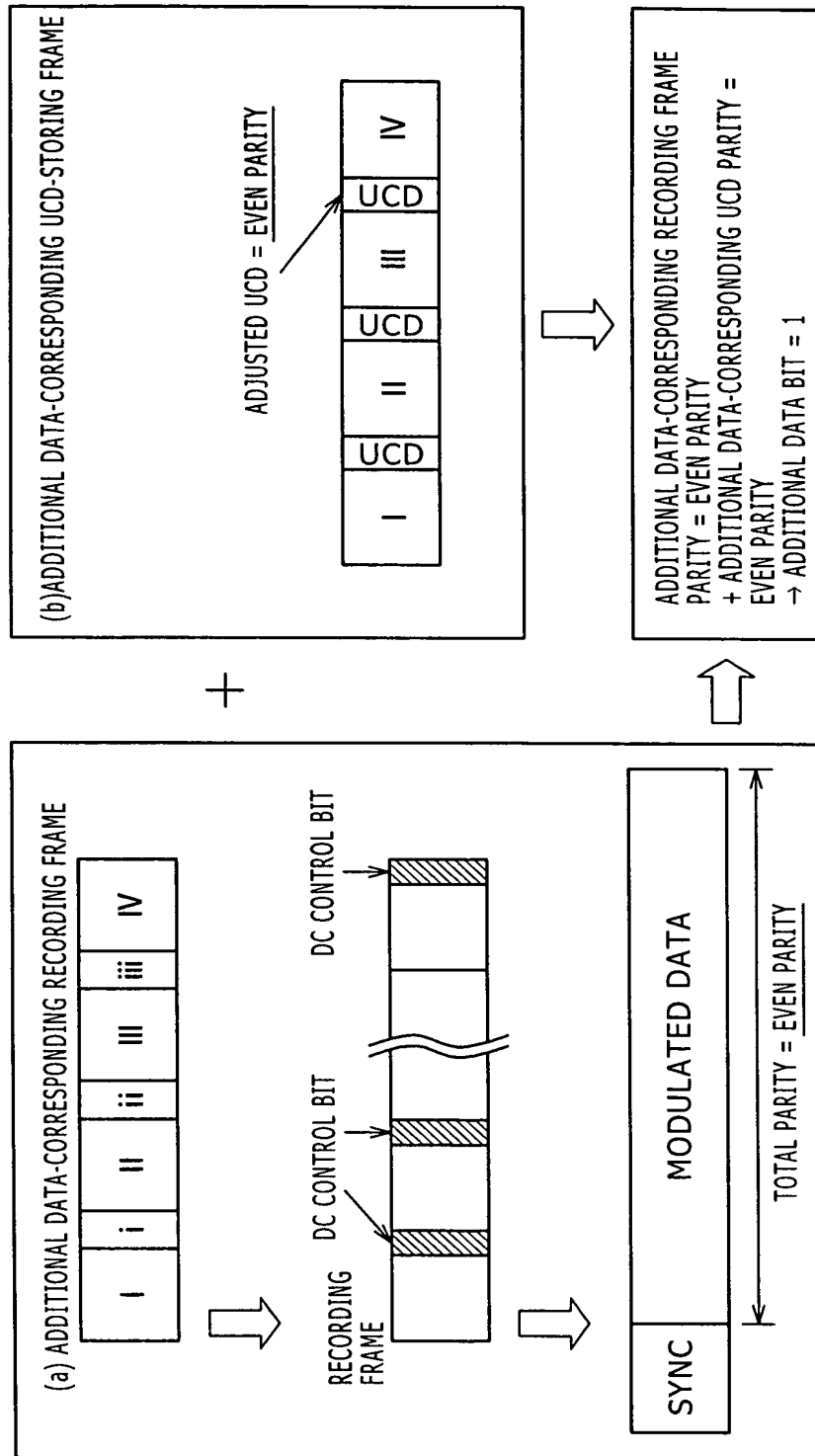
(2) ADDITIONAL DATA BIT = 0
 ADDITIONAL DATA-CORRESPONDING RECORDING FRAME PARITY = ODD



11/16

FIG. 11

(3) ADDITIONAL DATA BIT = 1,
 ADDITIONAL DATA-CORRESPONDING RECORDING FRAME PARITY = EVEN



12/16

FIG. 12

(4) ADDITIONAL DATA BIT = 1,
 ADDITIONAL DATA-CORRESPONDING RECORDING FRAME PARITY = ODD

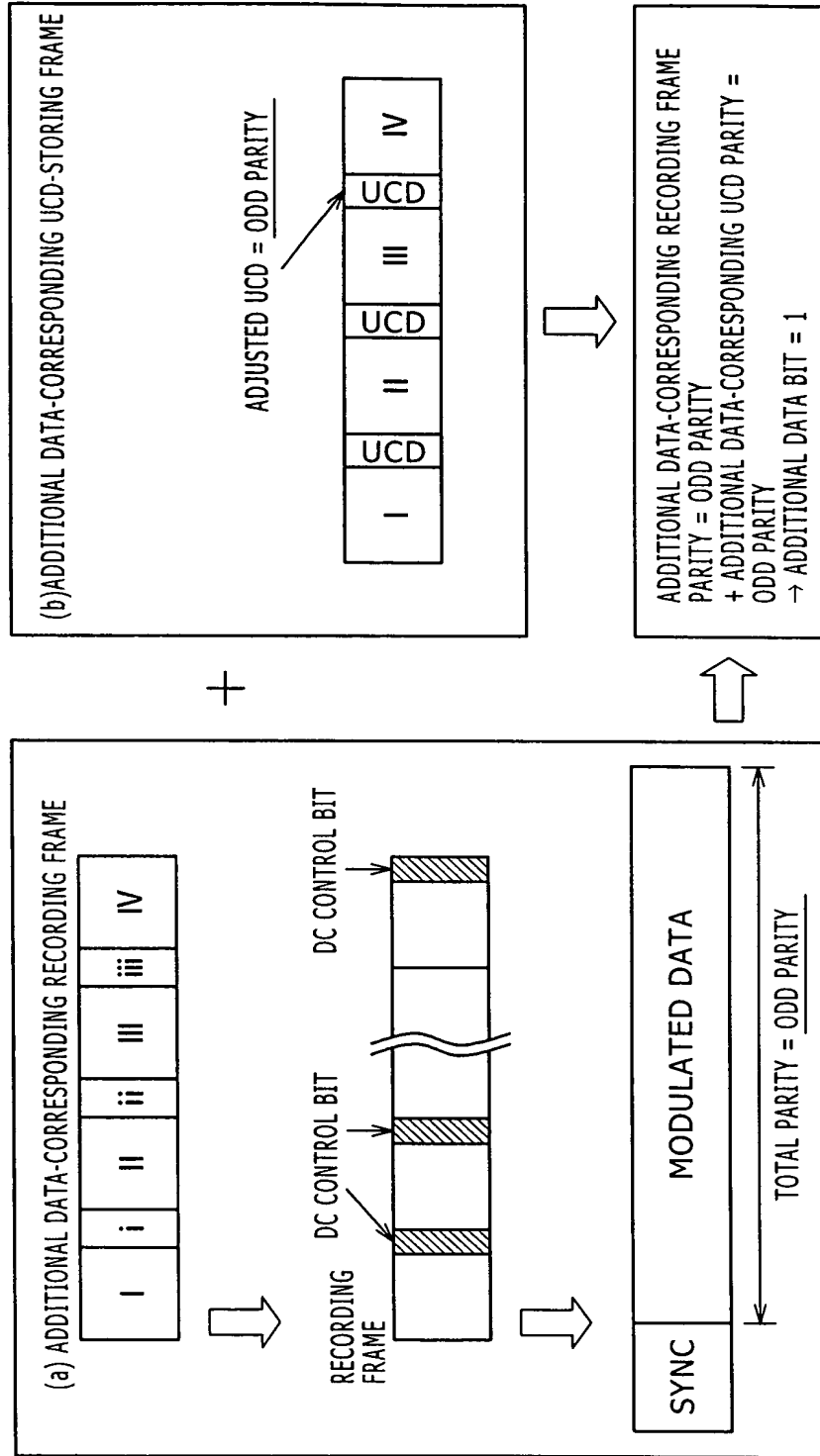


FIG. 13

	ADDITIONAL DATA BIT	PARITY OF ADDITIONAL DATA-CORRESPONDING RECORDING FRAME	PARITY OF ADDITIONAL DATA- CORRESPONDING UCD
1)	0	EVEN	ODD
2)	0	ODD	EVEN
3)	1	EVEN	EVEN
4)	1	ODD	ODD

14/16

FIG. 14

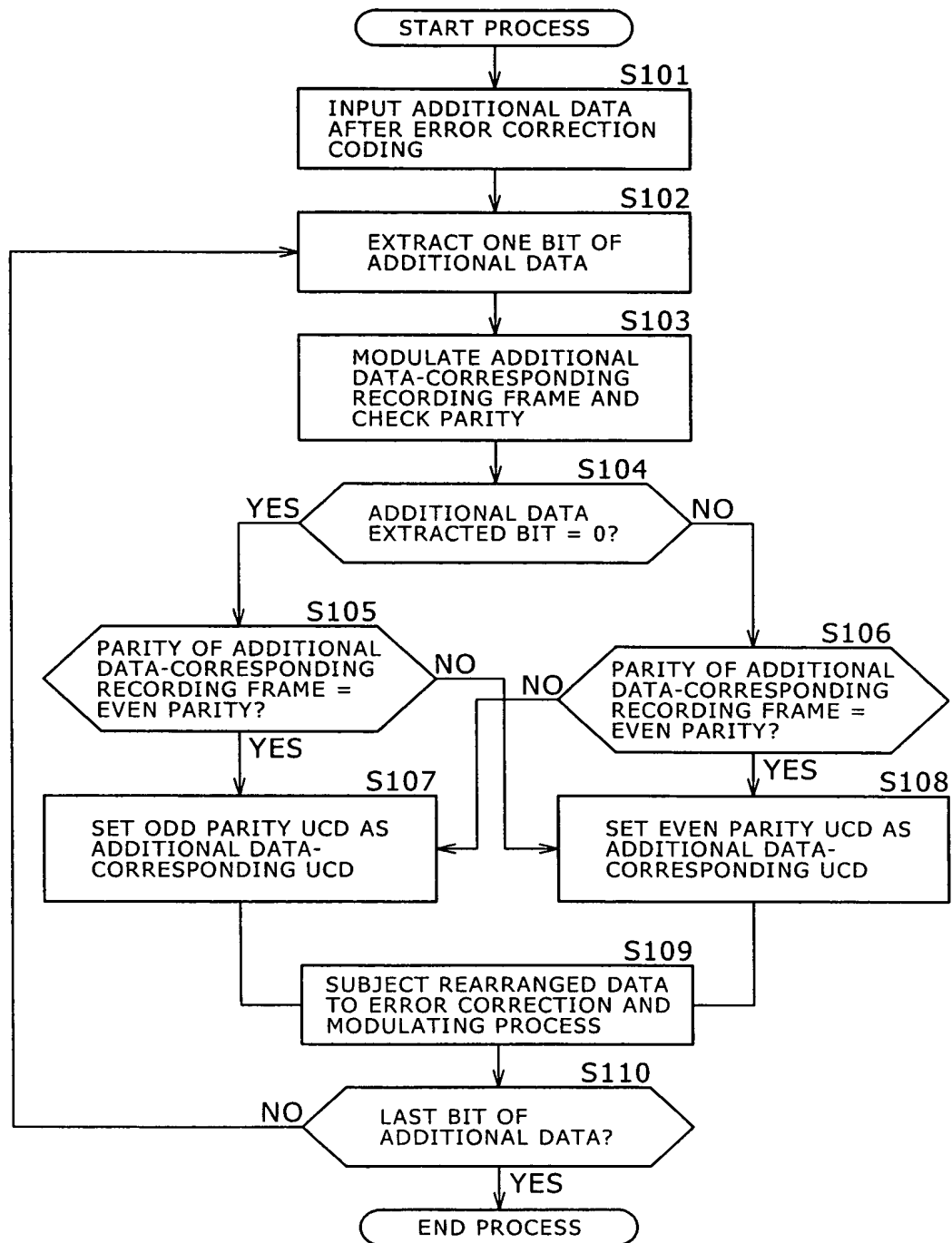
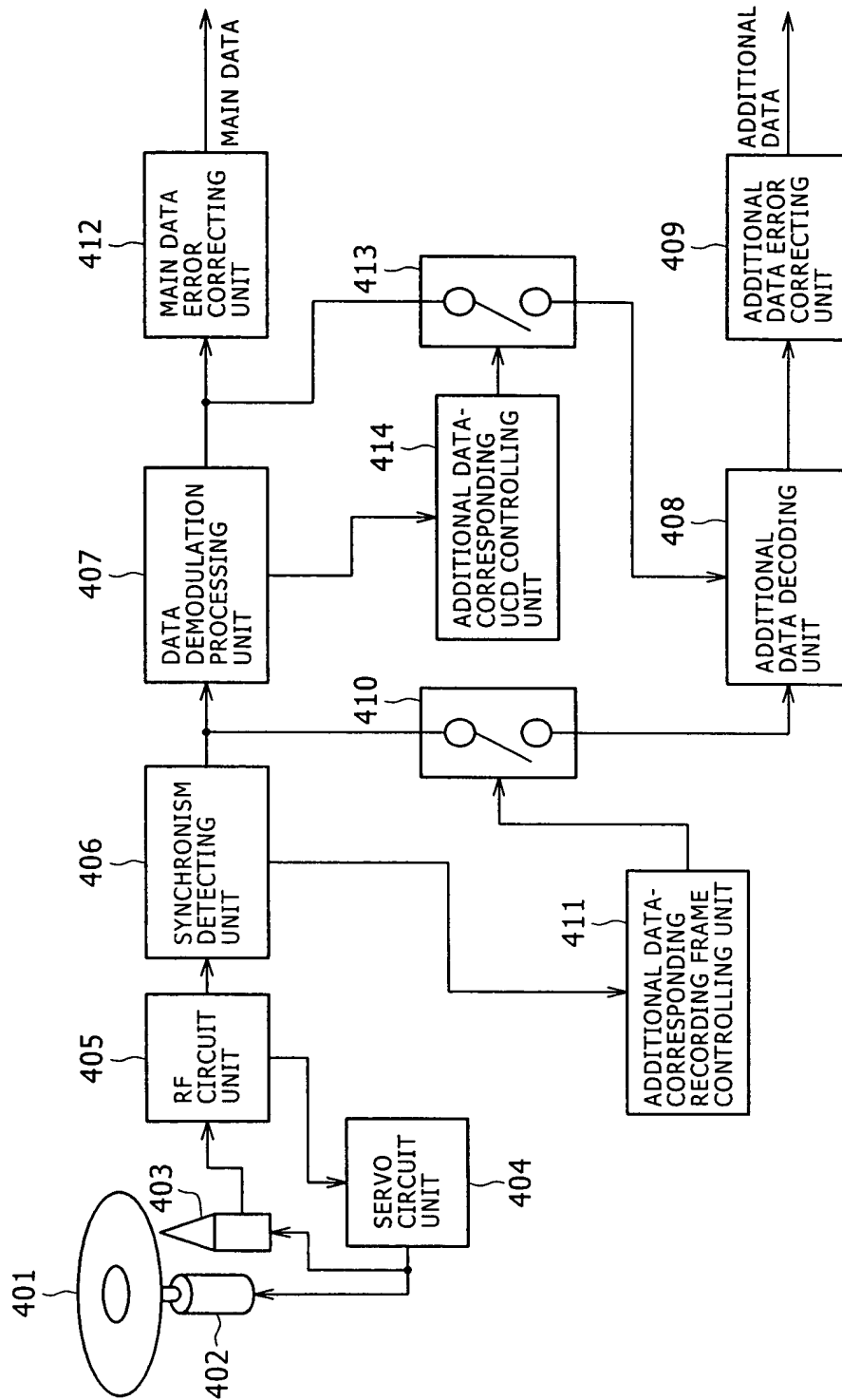


FIG. 15



16/16

FIG. 16

